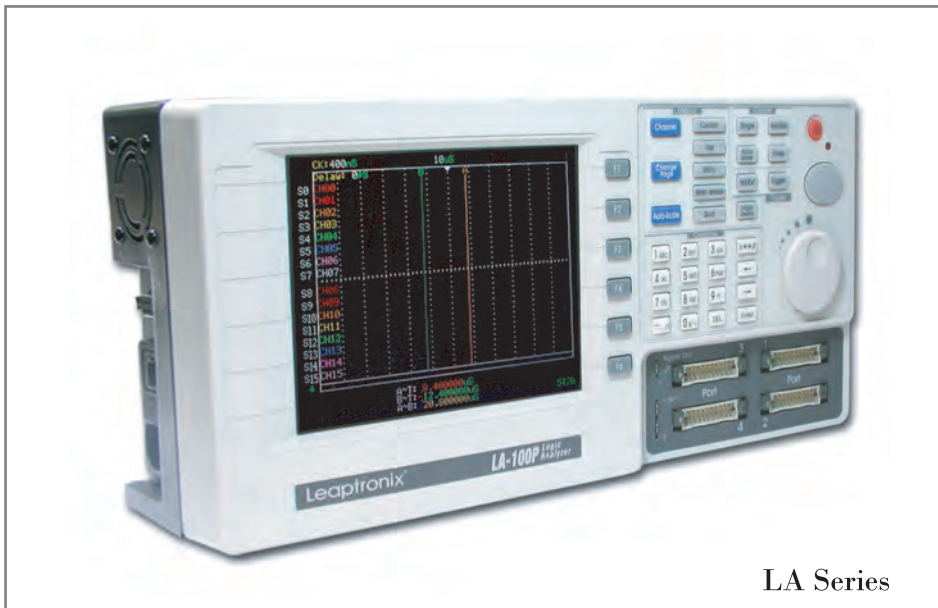


Stand-alone logic analyzer series



LA Series

The LA Series help minimize your project risk by providing the most reliable, accurate data capture and the most complete view of system behavior. These products are ideally suited for you on hardware/software debug, parametric and mixed signal testing and complex debugging. Moreover, their compact size and ability to connect to a PC makes them an ideal solution for use at remote sites.

Features

- External(asynchronous) and internal (synchronous) capture : Offer a more convenient environment for engineers.
- Provide three sets searching data functions and six cursor marks. The timing of each cursor mark to the trigger can be shown.
- Binary code and hexadecimal List mode (State) display
- Able to save the measuring data and waveform completely in stand-alone mode.
- Offers I²C, SPI, UART etc. signal decode function on PC based.
- Provide various signal trigger and capture: Pattern / Edge / AND / OR, four kinds of trigger modes.
- Pre-trigger, 3 level Post-trigger and continued-trigger functions make user operate more easily.
- Bus analysis and glitch capture function
- 2M Bytes~4M Bytes long memory depth; each CH memory depth is up to 512Kbits~1Mbits
- The adjustable sample rate size can be set by users, which avoid capture time too long.
- Provide "Trigger Counter" and "Pulse Wide Trigger" function
- High-speed Zoom In / Zoom Out technique
- Smart software provides text file for saving the Binary Code of waveform, which loads MATLAB easily
- Compact, portable for engineers' carry out debugging.
- 5.6 inch TFT color LCD display (LA-2025 / LA-2050)
- USB 2.0 interface for PC-base function, which can connect with PC for user to save, analyze, view and printout.

Application

- Development, measurement and quality control for digital products.
- Electronic, Electric machinery, Communication, Biotechnology and Medical technology digital products

Physical Specification

Timing	250MHz,Max 500MHz
State	100MHz,Max 200MHz
Bandwidth	100MHz/200MHz
Channels	32CH
RAM Size	2~4MBytes
Storage Depth per Channel	512K~1Mbits x32CH
Maximum Input Voltage	± 30V
Threshold Range	-4V~ +4V
Support PC-Link function	For Win-98/2k/XP
Data Skew (Channel to Channel)	1~2ns typical(4ns max)
PC-Link Interface	USB 2.0
AC-In	AC:90~240V/50~60Hz
Temperature	0°C~45°C
Dimension	31(W) x 15(H) x 9(D)cm
Weight	3.8kg

Standard Accessories

Main Unit.....	x1
Data Pod.....	x4
Lead Set	x4
Test Probe.....	x36
AC power cord.....	x1
User's manual.....	x1
CD.....	x1
USB 2.0 Cable.....	x1

LA Series Product Specification

Provide the best measurement solution

Model	LA-100P	LA-2025	LA-2050
Timing Analysis	250MHz,max(4ns)	250MHz,max(4ns)	500MHz,max(2ns)
State Analysis	100MHz~1Hz(10ns~1sec)	200MHz(Max)	200MHz(Max)
Bandwidth	100MHz	200MHz	200MHz
Channels	32CH	32CH	32CH
MEMORY			
RAM Size	2M Bytes	2M Bytes	4M Bytes
Storage Depth per Channel	512Kbits x 32CH	512Kbits x 32CH	1Mbits x 32CH
TRIGGER			
Condition	Pattern / Edge / AND / OR	Pattern / Edge / AND / OR	Pattern / Edge / AND / OR
Channels	32CH	32CH	32CH
Pre/Post Trigger	YES	YES	YES
Trigger Levels	3 (Edge or Pattern)	3 (Edge or Pattern)	3 (Edge or Pattern)
Continue Trigger	YES	YES	YES
Trigger out	YES (TTL Level)	YES (TTL Level)	YES (TTL Level)
Bus Analysis	YES	YES	YES
Glitch Capture	YES	YES	YES
Threshold Range			
Range	-4V~+4V	-4V~+4V	-4V~+4V
Accuracy	±50mV	±50mV	±50mV
Maximum Input Voltage	±30V	±30V	±30V
Impedance	100K Ω shunted by \approx 8pF	100K Ω shunted by \approx 8pF	100K Ω shunted by \approx 8pF
Temperature			
Operating	0°C~45°C(41°F~113°F)	0°C~45°C(41°F~113°F)	0°C~45°C(41°F~113°F)
Storage	-40°C~75°C(-56°F~167°F)	-40°C~75°C(-56°F~167°F)	-40°C~75°C(-56°F~167°F)
Data Skew(Channel to Channel)	2ns typical, \pm 4ns Max	2ns typical, \pm 4ns Max	1ns typical,2ns Max
PC-Link Interface	USB 2.0	USB 2.0	USB 2.0
Power			
Power Source	AC:90~240V,50~60Hz	AC:90~240V,50~60Hz	AC:90~240V,50~60Hz
Power Dissipation	18W	18W	18W
Max Power Dissipation	20W	20W	20W
Dimension			
(W) x (H) x (D) cm	31cm x 15cm x 9cm	31cm x 15cm x 9cm	31cm x 15cm x 9cm
Weight	3.8kg	3.8kg	3.8kg